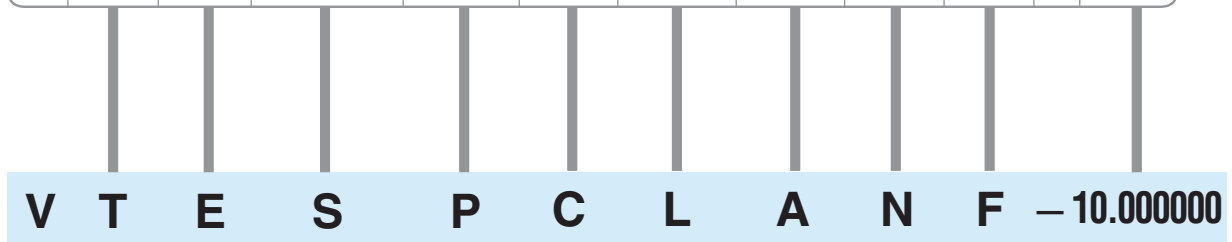


## Model Numbering Guide – VCXO

### Available options

Type	package (mm)	Supply Voltage(V)	Tri-State Function	Freq.Stability/APR (ppm)	Temp. Range(°C)	Output Logic and Symmetry	Oscillator Mode	Appearance	Lead Free	Dash	Freq. (MHz)
V: VCXO	A:3.2x2.5 W: 5.0x3.2 (6 Pads) T: 7.0x5.0 (6 Pads) D:7.0x5.0 (8 Pads) K: 14.2x9.3 (6 Pads) L: 14.0x9.0 (4 Pads)	C: 5 (Only for L Package) E: 3.3 J:2.5 K: 1.8 (Only for CMOS and Frequency < 60MHz)	U: Relative Pulling (Refer to Center Voltage) with Tri-State to pin 2 M: Multiplier Frequency with Tri-State to pin 2 S: Enable Low R: Input to pin 5 F: Without Tri-State	M: ±25/±50 (VC=10%Vdd ~90%Vdd) P: ±50/±50 (VC=10%Vdd ~90%Vdd) A: ±50/±50 (VC=0V~Vdd) B: ±25/±50 (VC=0V~Vdd) V: ±30/±30 W: ±25/±30	I: -10~+60 C: -20~+70 L: -40~+85 J: -40~+105	J: CMOS 15pF / 50±5% F: CMOS 50pF / 50±5% L: LVPECL / 50±5% V: LVDS / 50±5% W: Sine Wave M: CML	A: AT Fundamental T: AT 3 <sup>rd</sup> Overtone Not selectable by Customer	N: Normal F: Option A G: Option B J: Option C	F: RoHs Compliant	-	XX.XXXXXX



\*Not all combinations of options are available.

### Example: VTESPCLANF-10.000000

<b>Type</b>	VCXO
<b>Package</b>	7.0 x 5.0 mm
<b>Supply Voltage(V)</b>	3.3 V
<b>Tri-State</b>	Enable Low
<b>Freq. Stability / APR</b>	±50ppm / ±50ppm
<b>Temp Range</b>	-20~+70 °C
<b>Output</b>	LVPECL/Symmetry 50±5%
<b>Oscillator Mode</b>	AT Fundamental
<b>Appearance</b>	Normal Appearance
<b>Lead Free</b>	RoHs Compliant
<b>Frequency</b>	10.000000 MHz