Application Notes: Utilize Clipped Sine Waveform in Circuit Design
**Clipped Sine Waveform Output**

- Clipped sine waveform is the most popular output option for TCXOs.
  - Low power consumption for improved thermal characteristics → Better aging and frequency stability performance
  - Better phase noise performance than CMOS output
• To convert the clipped sine waveform to a square waveform signal such as CMOS, an input buffer is necessary.
• Generally, ICs that TCXO’s output going into would have input buffer functionality integrated inside the chip. Oscillator circuit should have Rf feedback resistor connected in parallel with the inverter.
• If the IC does not have Rf resistor inside then an Rf resistor would need to be added externally between input pin and output pins of the inverter.
• The external Rf resistor value shall be between 100K to 10M ohm.
• Except for the 7x5mm product lines, a 1000 pF capacitor needs to be placed in series between the TCXO output pin and the IC clock input pin for all the other smaller form-factor TAITIEN TCXOs.
If the IC does not have input buffer functionality incorporated, an inverter such as 74HC04 and an Rf resistor would need to be added in series between the output pin of the TCXO and the clock input pin of the IC to convert the clipped sine waveform to a square waveform.

- The external Rf resistor value shall be between 100K to 10M ohm.

- Except for the 7x5mm product lines, a 1000 pF capacitor needs to be placed in series between the TCXO output pin and the IC clock input pin for all the other smaller form-factor TAITIEN TCXOs.
• TAITIEN TCXO with clipped sine wave output can also be used in conjunction with an oscillator IC to generate square wave output for the chip set.

• The output from TCXO needs to be connected to the crystal input pin on the oscillator IC and the output pin from the oscillator IC shall be connected to the input pin on the chip set.

• Except for the 7x5mm product lines, a 1000 pF capacitor needs to be placed in series between the TCXO output pin and the IC clock input pin for all the other smaller form-factor TAITIEN TCXOs.