Application Note: How to Minimize EMI effect on Crystal Oscillators
For TATIEN crystal oscillators, top metal covers are electrically connected to GND terminals. The metal covers provide effective shielding against EMI from ASIC inside the oscillator.
Voltage spikes seen on output signals from crystal oscillators. This can contribute to EMI issues in the application circuits.
Adding a bypass capacitor (5 to 10 pF) between the output terminal and GND would smooth out the output waveform and reduce EMI significantly.
Reducing EMI from Crystal within Oscillator Circuit

Terminals 2 and 4 on a TAITIEN crystal unit are electrically connected to the metal cover of the crystal. It is recommended to connect terminals 2 and 4 to GND to minimize EMI.